

Voltage Sag Mitigation by Using Dynamic Voltage Restorer

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Abstract— Power quality is one of major concerns in the present era. It has become important, especially, with the introduction of sophisticated devices, whose performance is very sensitive to the quality of power supply. Power quality problem is an occurrence manifested as a nonstandard voltage, current or frequency that results in a failure of end use equipment. One of the major problems dealt here is the power sag. To solve this problem, custom power devices are used. One of those devices is the Dynamic Voltage Restorer (DVR), which is the most efficient and effective modern custom power device used in power distribution networks. Its appeal includes lower cost, smaller size, and its fast dynamic response to the disturbance. This paper presents modeling, analysis and simulation of a Dynamic Voltage Restorer (DVR) using MATLAB. In this model a PI controller and Discrete PWM pulse generator was used.

Index Terms — DVR, voltage dips, swells, interruption, power quality, VSC.

I. INTRODUCTION

Power quality is becoming an increasingly important topic in the performance of many industrial applications such as information technology, significant influence on high technology devices related to communication, advanced control, automation, precise manufacturing technique and on-line service. Users need constant sine wave shape, constant frequency and symmetrical voltage with a constant root mean square (rms) value to continue the production. To satisfy these demands, the disturbances must be eliminated from the system. The typical power quality disturbances are voltage sags, voltage swells, interruptions, phase shifts, harmonics and transients [1][2]. Among the disturbances voltage sag is considered the most severe since the sensitive loads are very susceptible to temporary changes in the voltage. Voltage sag is a short-duration reduction in voltage magnitude. The voltage temporarily drops to a lower value and comes back again after approximately 150ms. Despite their short duration, such events can cause serious problems for a wide range of equipment [1][3]. The characterization of voltage sags is related with:

1. The magnitude of remaining voltage during sag
2. Duration of sag

In practice the magnitude of the remaining voltage has more influence than the duration of sags on the system. Voltage sags are generally within 40% of the nominal voltage in industry. Voltage sags can cost millions of dollars in damaged product, lost production, restarting expenses and danger of breakdown [2][3].

Short circuit faults, motor starting and transformer energizing will cause short duration increase in current and this will cause voltage sags on the line. For certain end users of sensitive equipment the voltage correction device may be the only cost-effective option available.

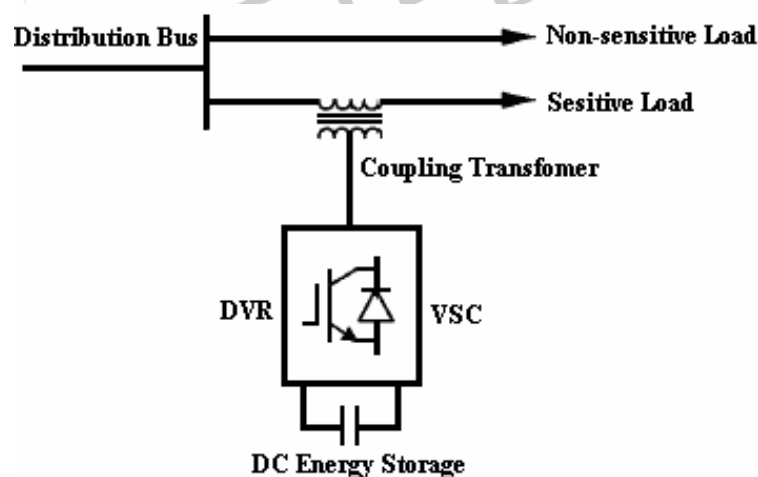


Fig.1: Basic Configuration of DVR

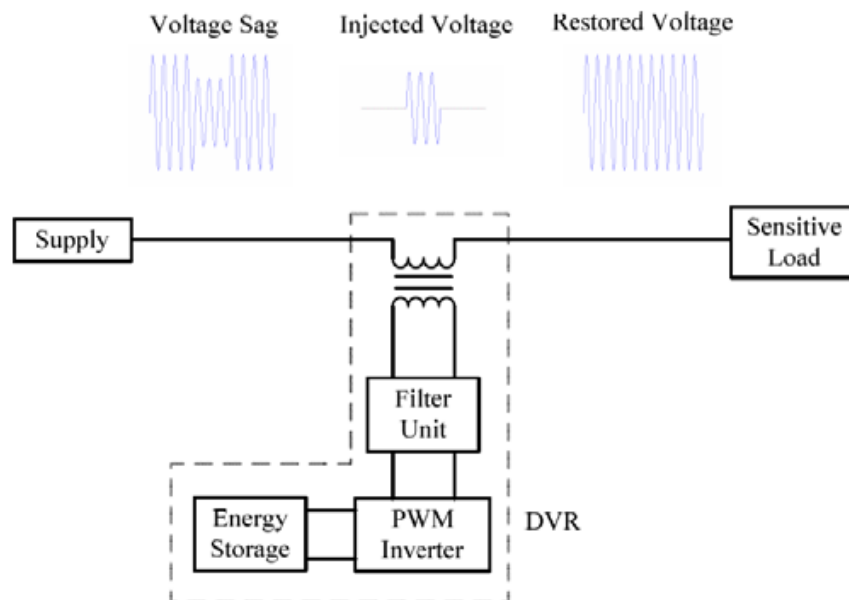


Fig.2: Typical application of DVR in distribution system

Different approaches exist to limit the costs caused by voltage dips and one interesting approach considered here is to use voltage source converters connected in series between the supply system and the sensitive load, this type of devices are often termed a Dynamic Voltage Restorer (DVR). Unlike uninterruptible power supply (UPS), the DVR is specifically designed for large loads ranging from a few MVA up to 50 MVA or higher [8]. The DVR is fast, flexible and efficient solution to voltage sag problems. It can restore the load voltage within a few milliseconds and hence avoiding any power disruption to that load. The main idea of the DVR is detecting the voltage sag and injecting the missing voltage in series to the bus by using an injection transformer as shown in Figure 2.

The DVR can be divided into four component blocks, namely:

1. Voltage source PWM inverter
2. Injection/coupling transformer
3. Energy storage device
4. Filter unit.

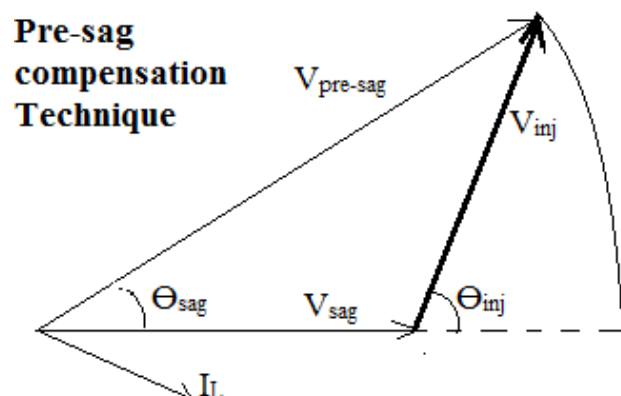
II. PROPERTIES CHOICE OF DVR

There are numerous reasons why DVR is preferred over other devices:

1. Although, SVC predominates the DVR but the latter is still preferred because the SVC has no ability to control active power flow.
2. DVR is less expensive compared to the UPS.
3. UPS also needs high level of maintenance because it has problem of battery leak and have to be replaced as often as five years.
4. DVR has a relatively higher energy capacity and costs less compared to SMES device.
5. DVR is smaller in size and costs less compared to DSTATCOM.
6. DVR is a power efficient device compared to the UPS..

III. CONVENTIONAL CONTROL STRATEGIES

Several control techniques have been proposed for voltage sag compensation such as pre-sag method, in-phase method and minimal energy control [2] [3].



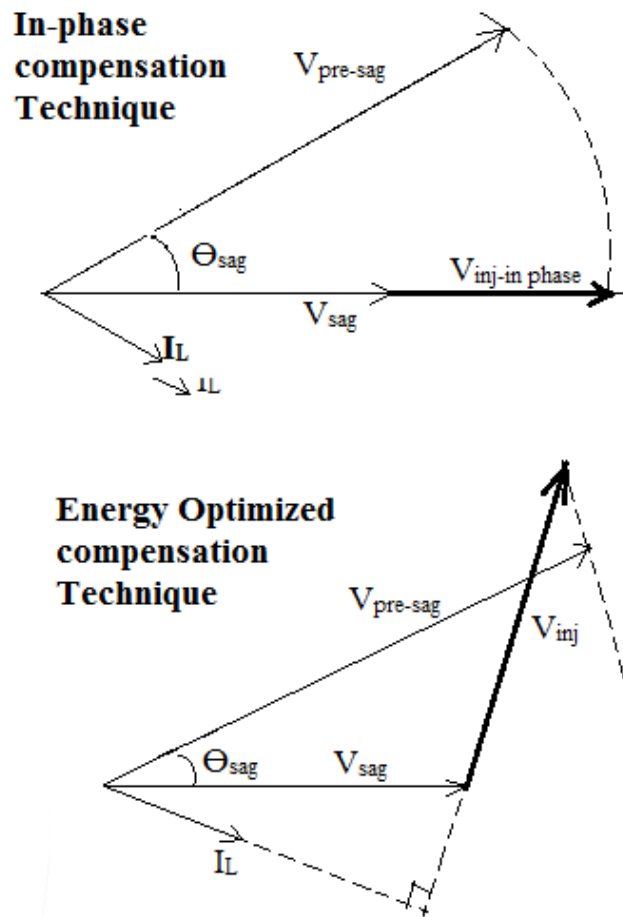


Fig.3: Conventional Control Strategies of DVR [9]

A. Pre-Sag Compensation Technique

The main defect of this technique is it requires a higher capacity energy storage device. Fig.2 shows the phasor diagram for the pre-sag control strategy in this diagram; $V_{pre-sag}$ and V_{sag} are voltage at the point of common coupling (PCC), respectively before and during the sag. In this case VDVR is the voltage injected by the DVR, which can be obtained as [9]:

$$V_{pre-sag} = V_L, V_{sag} = V_s \quad VDVR = V_{inj}$$

$$|V_{inj}| = |V_{pre-sag}| - |V_{sag}| \dots \dots (1)$$

$$\theta_{inj} = \tan^{-1} \left\{ \frac{V_{pre-sag} \sin(\theta_{pre-sag})}{V_{pre-sag} \cos(\theta_{pre-sag}) - V_{sag} \cos(\theta_{sag})} \right\}$$

B. In-Phase Compensation Technique

In this technique, only the voltage magnitude is compensated. VDVR is in-phase with the left hand side voltage of DVR. This method minimizes the voltage injected by the DVR, unlike in the pre-sag compensation. Fig.2 shows phase diagram for the in-phase compensation technique [9].

$$VDVR = V_{inj}$$

$$|V_{inj}| = |V_{pre-sag}| - |V_{sag}|$$

$$\angle V_{inj} = \theta_{inj} = \theta_s$$

C. Energy Optimized compensation Technique

Pre-sag compensation and in-phase compensation must inject active power to loads almost all the time. Due to the limit of energy storage capacity of DC link, the DVR restoration time and performance are confined in these methods. The fundamental idea of energy optimization method is to make injection active power zero. In order to minimize the use of real power the voltages are injected at 90° phase angle to the supply current. Fig.2 shows a phasor diagram to describe the Energy optimization Control method.

The selection of one of these strategies influences the design of the parameters of DVR. In this paper, the control strategy adopted is Pre-sag compensation to maintain load voltage to pre fault value [9].

IV. MATHAMATICAL MODEL

During the standby mode (non-faulted times) DVR will do nothing and it should be ensured that the load is not disturbed by the DVR. If the voltage drop caused by the injection transformer exceeds the limit, DVR may inject small voltage to compensate fault. In standby mode, DVR only produces conduction losses because no voltage source PWM inverter switching takes place.

In the event of a fault on the load side of the DVR, a fast rising current surge through the low voltage side of the injection transformer and the voltage source PWM inverter occurs. The protection system guarantees that power semiconductor switching can take place when current levels exceed the switching capability of the power semiconductor. In the event that a critical current level is detected the DVR is bypassed. The bypass system protects DVR components from abnormally high downstream load or fault currents. The DVR should have clever bypass schemes so as not to create additional disturbances onto the system, which will affect the load.

When voltage sag depth to be corrected is between the ratings of DVR, DVR starts to generate the equivalent missing voltage and inject it to the line. DVR will continue the injection process until the bus voltage reaches its pre-fault value. DVR will inject three single-phase ac voltages with controllable amplitude and phase. The voltage sags resulting from faults can be corrected either in the transmission or distribution system. DVR can compensate small disturbances by injecting reactive power and compensate larger disturbances by injecting real power to the system.

To obtain missing voltage DVR compares the distorted source voltage with its pre-fault value and then generate the control signal for PWM. The control unit gives information on required voltage to be inserted and its duration during sag. Sinusoidal pulse width modulation technique is used to control DVR. Solid-state power electronic switching devices are used in PWM [10]. The output of PWM may contain harmonics and they can be filtered on the inverter side or the line side to smooth the voltage waveform. The filtering scheme should keep the total harmonic distortion (THD) of the remaining voltage at the supply side and the injected voltage within limits determined by standards.

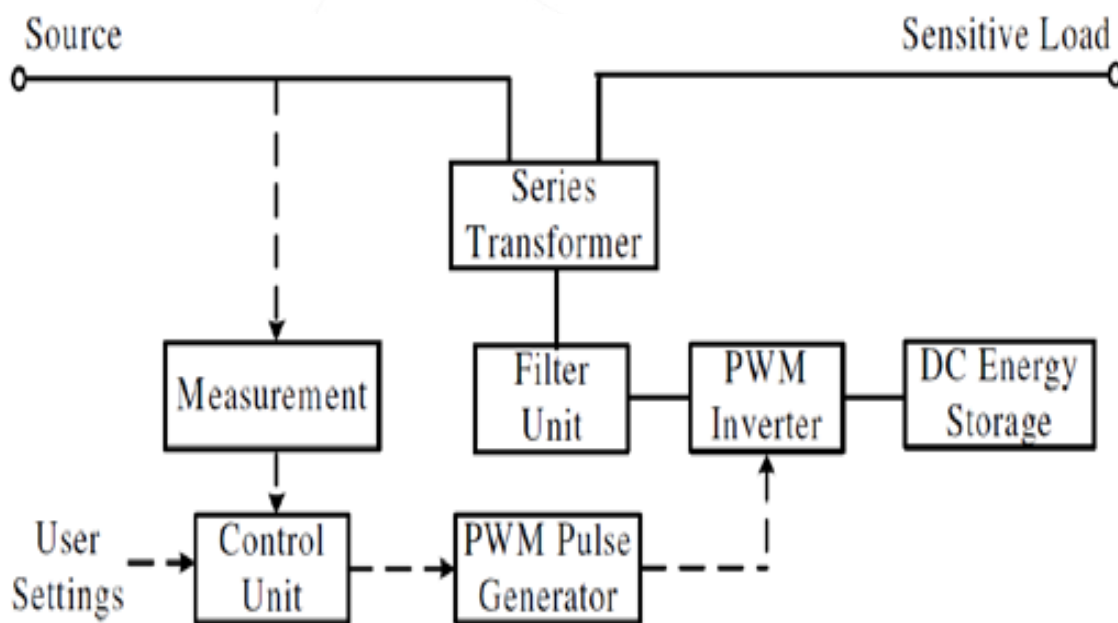


Fig.4: Simplified DVR block

Voltage restoration of DVR needs to inject energy from DVR to distribution system. The required energy for injection during sag may be supplied from the grid or energy storage devices such as batteries or super conducting magnetic energy storage systems. However, the capability of energy storage that usually consists of capacitors in DVR is limited. Therefore, it must be considered how the injection energy can be minimized during deep voltage sags and the load voltage can be made close to pre-fault voltage. This strategy is known as dynamic voltage restorer with minimum energy injection [5][7][10].

The DVR is a series connected device onto the system and it acts as an additional energy source. Carefully design considerations must be taken to integrate the device into the system. Figure.3 can mainly summarize the main operation principle of DVR.

V. TEST SYSTEM FOR DVR

Single line diagram of the test system shown in the figure.5 for DVR is composed by a 13 kV, 50 Hz generation system, feeding two transmission lines through a 3- winding transformer connected in Y/S/S, 13/115/115 kV. Such transmission lines feed two distribution networks through two transformers connected in S/Y, 115/11 kV. To verify the working of DVR for voltage compensation a fault is applied at point X at resistance 0.66 U for time duration of 200 ms. The DVR is simulated to be in operation only for the duration of the fault [5].

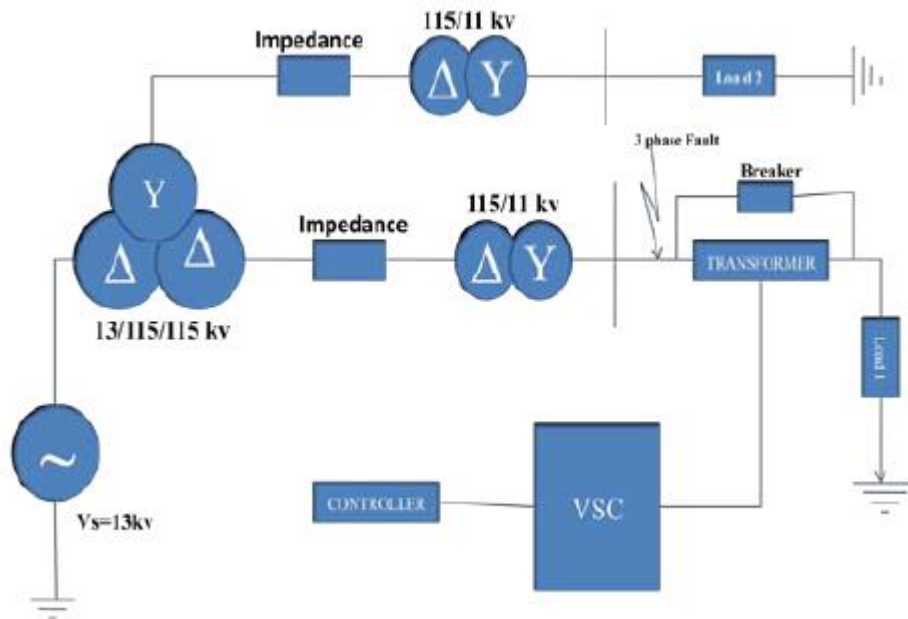


Fig.5 Single line diagram of the test system for DVR

VI. SIMULATION RESULT

The first simulation was done with no DVR and a three phase fault is applied to the system at point with fault resistance of $0.66 U$ for time duration of 200ms. The second simulation is carried out at the same scenario as above but a DVR is now introduced at the load side to compensate the voltage sag occurred due to the three phase fault applied. Figure.7 shows the RMS voltage at load point when the system operates with no DVR

and a three phase fault is applied to the system. When the DVR is in operation the voltage interruption is compensated almost completely and the RMS voltage at the sensitive load point is maintained at normal condition.

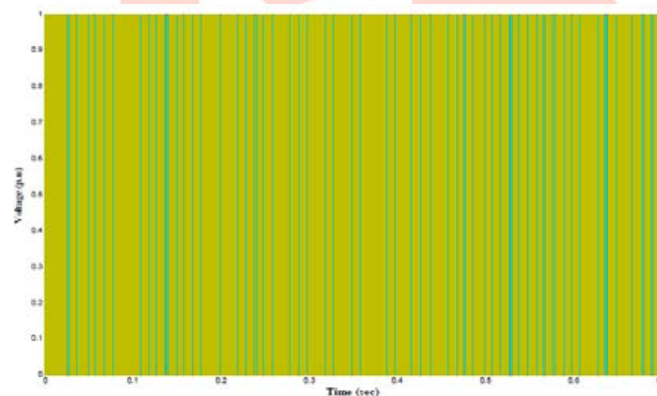


Fig.6 Firing pulse generated by discrete PWM generator

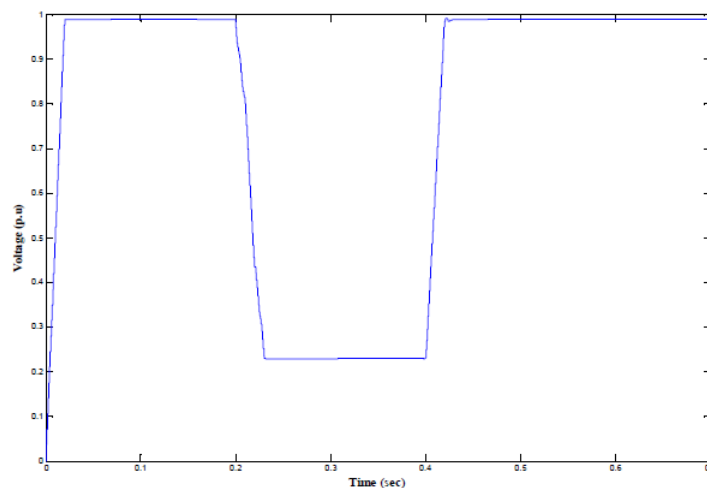


Fig.7 pu Voltage at load point, with 3-Ø fault, without DVR

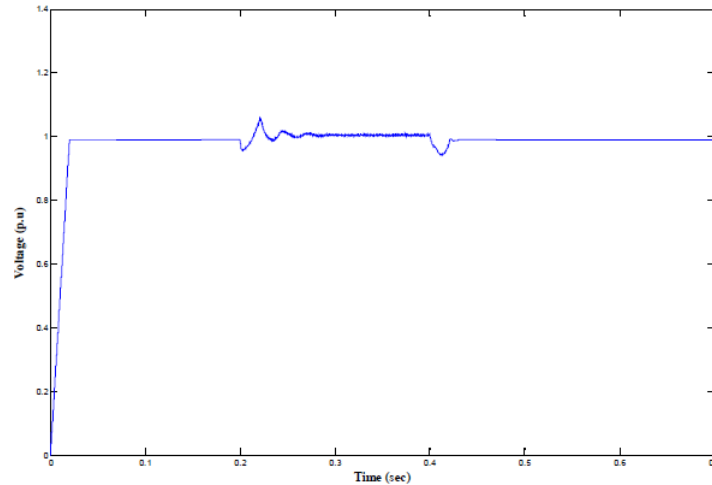


Fig.8 Pu Voltage at load point, with 3-Ø fault, with DVR

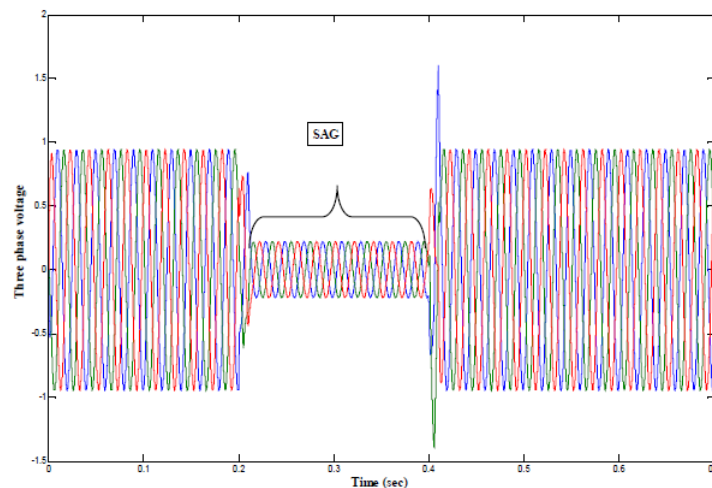


Fig.9 3-Ø Voltage at load point, with 3-Ø fault, without DVR

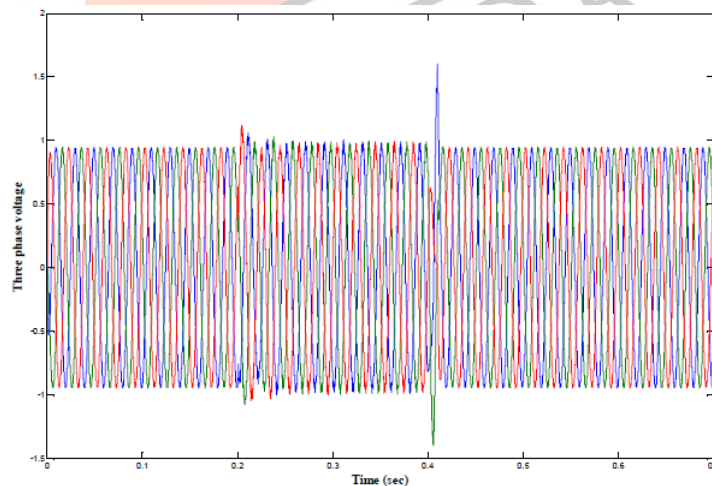


Fig.10 3-Ø Voltage at load point, with 3-Ø fault, with DVR

VII. CONCLUSION

This paper has presented the power quality problems such as voltage dips, swells, distortions and harmonics. Compensation techniques of custom power electronic devices DVR was presented. The design and applications of DVR for voltage sags and comprehensive results were presented. A PWM-based control scheme was implemented. As opposed to fundamental frequency switching schemes already available in the MATLAB/ SIMULINK, this PWM control scheme only requires voltage measurements. This characteristic makes it ideally suitable for low-voltage custom power applications [4] [6].

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