

# Unified Power Quality conditioner in Grid connected Photovoltaic System

<sup>1</sup>Sukhjinder Singh, <sup>2</sup>Robinjit Singh, <sup>3</sup>Mukul Chankaya  
<sup>1</sup>Student M.Tech, <sup>2</sup>Student M.Tech, <sup>3</sup>Assistant Professor  
<sup>1</sup>Department of Electrical Engineering,  
<sup>1</sup>Lovely Professional University, Phagwara, Punjab

**Abstract** – in this paper power quality of a distributed generation network consisting of photovoltaic generation as a distributed generation source is improved with the help of unified power quality conditioner. The control strategy which is applied in this paper is based on synchronous reference frame theory. So in this control strategy the reference signal is obtained by SRF theory and the comparison of reference signal is done to find the error signal, and this error signal is used to generate gate pulses with the help of PWM technique.

**Index Terms** – UPQC, Active Power Filters, PWM, SRF, Photovoltaic Generation, Distributed Generation, power Quality.

## I. INTRODUCTION

The use of non conventional energy sources is increasing as distributed generation; this type of distributed generation can cause several power quality problems in to grid. As the photovoltaic power is connected to grid through inverters so it will inject harmonics into grid supply. The second cause of disturbance can be due to variable solar radiations due to which the output of photovoltaic system will not be steady and this will create fluctuations in line voltage. Third source of disturbance is the faults in DG (Distributed Generation). Whenever any fault occurs in distributed generation system it affects the supply of main line. So these are some power quality problems which can occur in distribution grid due to distributed generation. Custom power devices can be the solution to these power quality problems. There are several custom power devices like D-STATCOM, DVR and UPQC these devices can be used according to requirement of the network. D-STATCOM is used to compensate problems related to quality of current and also provides reactive power to load with lagging power factor [1]. DVR is dynamic voltage restorer and is used for improving voltage profile of line. When both these active power filters are combined then the combination is known as unified power quality conditioner. It has the ability to compensate both voltage and current related problems. In UPQC the D-STATCOM and DVR are connected through a common DC link and share power through this common DC link. In this system UPQC is connected at point of common coupling to stabilize voltage and reduce current harmonics [2]. The proposed system is shown below with the help of block diagram in fig.1.

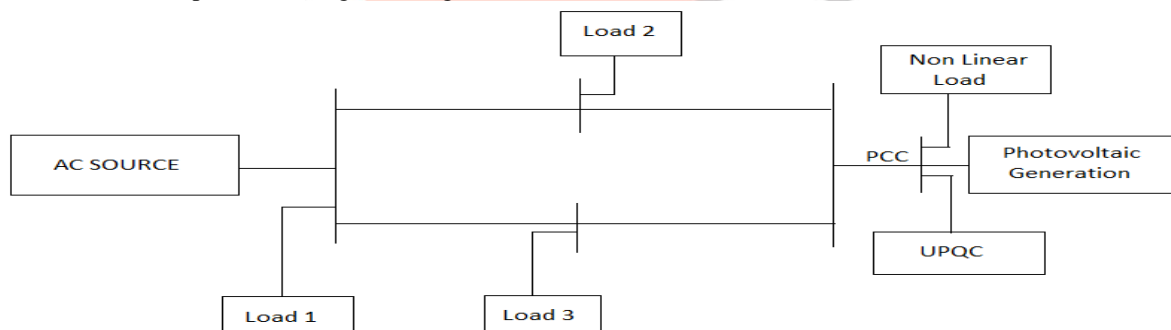


Fig.1UPQC in grid connected PV

The UPQC is already explained above. The second component which is used along with UPQC is photovoltaic array. Now the photovoltaic array containing 6 PV modules is shown below, in this 6 photovoltaic modules are used in series and the design of array is based on structure shown in Fig.2 [ECEN2060]. Photovoltaic array consisting of six modules is shown below and the design of photovoltaic module is based on the structure presented in library [ECEN2060], and is shown in Fig.3

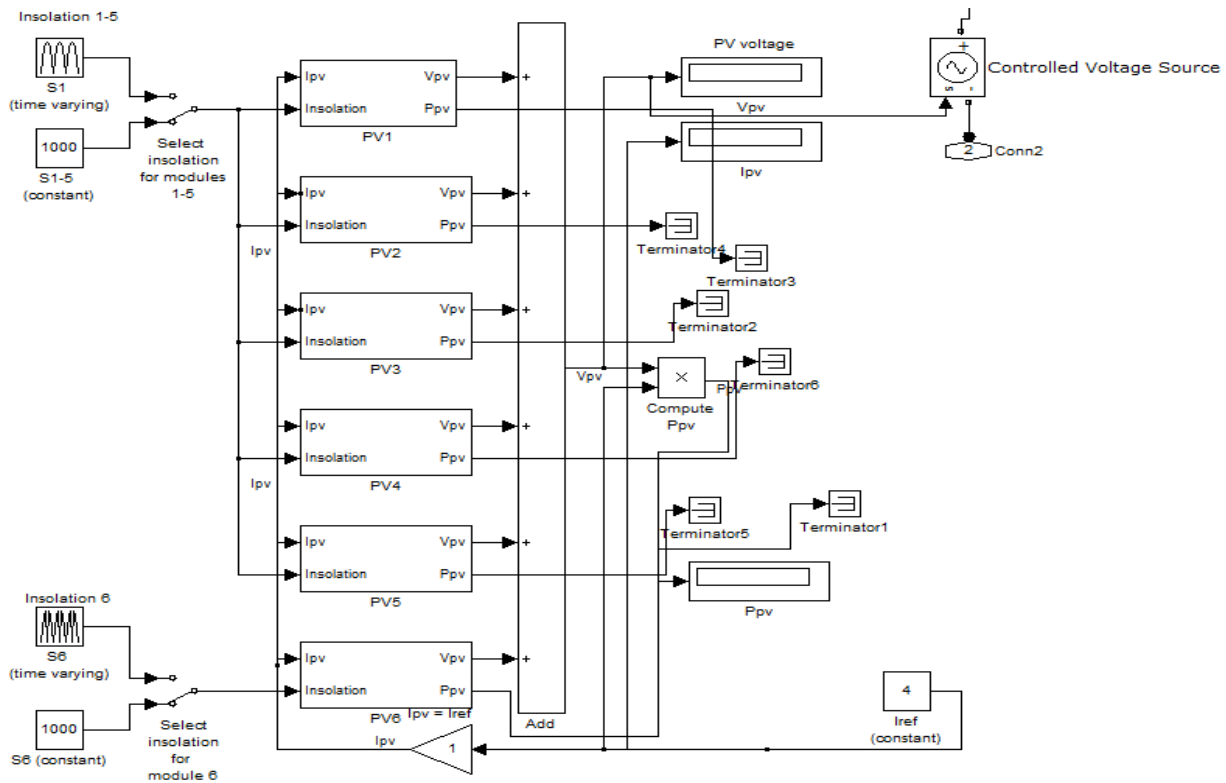


Fig.2 six module PV array

## II. SYNCHRONOUS REFERENCE FRAME THEORY

In this work both controllers for series and shunt APFs are controlled with the help of theory called synchronous reference frame (SRF) theory. This control scheme is used because it is simple in calculation as compared to other control schemes [12]. This control strategy is based on algebraic calculations so it is less complex as compare to other methods. It uses Park transformation and inverse park transformation to generate of reference signals for both active power filters i.e. series APF and shunt APF [15]. The transformation is done from (a-b-c) to (d-q-o) frame [3]. In this Sine and cosine functions are generated by phase locked loop (PLL) to maintain synchronism of compensated current and voltage with supply current and voltage. Under disturbed conditions the d and q axis voltage and current consists of both average components ( $\bar{i}_d$  and  $\bar{i}_q$ ) and oscillating components ( $\tilde{i}_d$  and  $\tilde{i}_q$ ) [2]. And if the system is in balanced state then it consist only positive sequence components. But during unbalanced conditions and with non linear load the system will have zero, negative and positive sequence components [14]. The equations given below are used in transformation and inverse transformation of quantities. First equation is used to convert voltage from a-b-c to d-q-0 frame.

$$V_d = 2/3[V_a \sin(\omega t) + V_b \sin(\omega t - 2\pi/3) + V_c \sin(\omega t + 2\pi/3)]$$

$$V_q = 2/3[V_a \cos(\omega t) + V_b \cos(\omega t - 2\pi/3) + V_c \cos(\omega t + 2\pi/3)]$$

$$V_0 = 1/3[V_a + V_b + V_c]$$

And to convert these components back to a-b-c frame the equation given on next page is used.

$$V_a = V_d \sin(\omega t) + V_q \cos(\omega t) + V_0$$

$$V_b = V_d \sin(\omega t - 2\pi/3) + V_q \cos(\omega t - 2\pi/3) + V_0$$

$$V_c = V_d \sin(\omega t + 2\pi/3) + V_q \cos(\omega t + 2\pi/3) + V_0$$

For three phase current same transformations will be used, we have to simply replace  $V_a, V_b, V_c, V_d, V_q$  and  $V_0$  with  $I_a, I_b, I_c, I_d, I_q$  and  $I_0$  variables.

## III. CONTROL SCHEME FOR SERIES APF

For series APF control the reference signal is generated with the help of SRF theory and phase locked loop (PLL). Park transformation and inverse park transformations are used to convert quantities from a-b-c to d-q-0 frame and again from d-q-0 to a-b-c frame to generate reference signal. In this control scheme the reference signal for series APF is obtained and compared with actual load voltage signal to generate gate pulses [6]. First of all three phase supply voltage  $V_{sabc}$  will be transformed to  $V_{sdq0}$ .

$$\begin{bmatrix} V_{sd} \\ V_{sq} \\ V_{so} \end{bmatrix} = T \begin{bmatrix} V_{sa} \\ V_{sb} \\ V_{sc} \end{bmatrix}$$

Where  $T$  represents the park transformation and is used to convert source voltage from a-b-c to d-q-0 frame. In this  $V_{sd}$  and  $V_{sq}$  have both oscillating components ( $\tilde{V}_{sd}$  and  $\tilde{V}_{sq}$ ) and average components ( $\bar{V}_{sd}$  and  $\bar{V}_{sq}$ ). Here oscillating components consists of harmonics and negative sequence components. But the average component consists of fundamental components of supply voltage only. The zero sequence components exists only in unbalanced state [4]. The reference signal will be compared with actual voltage (load) and gate pulses will be generated. To generate gate pulses we can use either PWM controller or hysteresis band controller [13]. Here in this work PI-PWM is used to generate gate pulses. When the load voltage differs from reference voltage then series controller gives signal to series inverter and it injects same amount of voltage in series with line [8]. The block diagram of series controller is shown below.

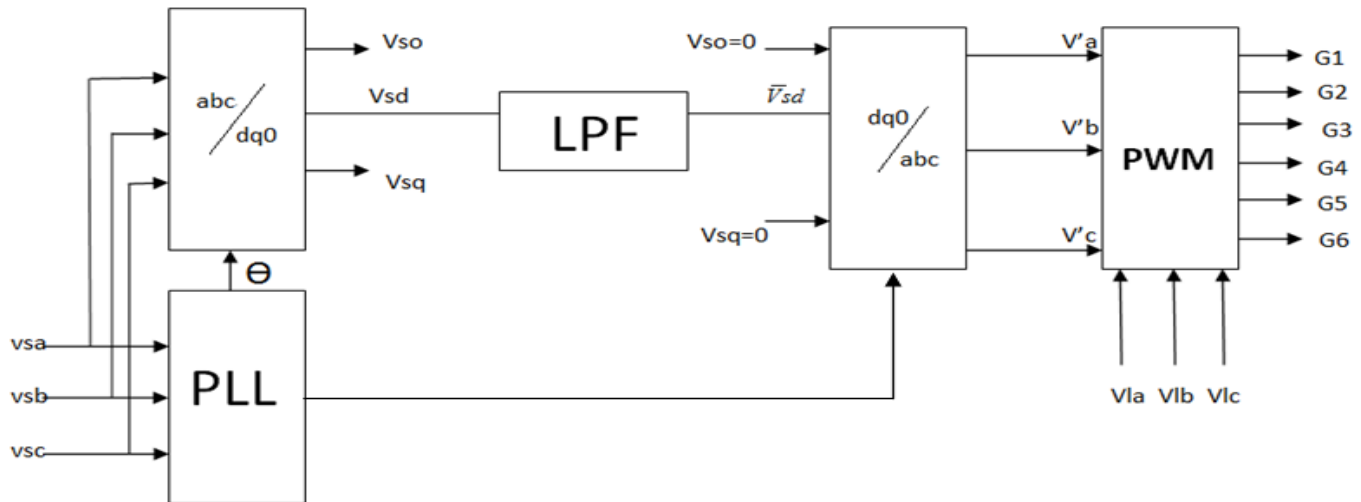


Fig.3 control scheme of series APF

#### IV. CONTROL SCHEME FOR SHUNT APF

Shunt APF injects the current opposite to harmonics current in equal amount as of harmonics current shifted at  $180^\circ$  and provide reactive power compensation for lagging power factor load and maintain DC link voltage at constant level [11]. The control strategy includes the transformation of source current from abc frame to dq0 frame with the help of parks transformation and again back to abc frame from dq0 frame by using inverse parks transformation. These transformations are already explained above. Now the dq0 transformation of source current is shown below [10].

$$\begin{bmatrix} isd \\ isq \\ iso \end{bmatrix} = T \begin{bmatrix} isa \\ isb \\ isc \end{bmatrix}$$

When the load is non linear then source current will have both average components ( $\bar{isd}$  and  $\bar{isq}$ ) and oscillating components ( $\tilde{isd}$  and  $\tilde{isq}$ ) [7]. We know that shunt APF is used to maintain DC link voltage along with providing compensations. So to maintain DC voltage level constant it absorbs some active power from network. In this actual DC link voltage is compared with a reference signal and the required active current  $I_{dloss}$  is calculated using PI controller[6]. And the reference current for APF is calculated by summing  $\bar{isd}$  and  $I_{dloss}$ . Therefore  $i'_{sd} = I_{dloss} + \bar{isd}$ . In this calculation only d axis components are taken for calculation of reference current and q axis and zero sequence components are set to zero. This is done to compensate harmonics and reactive power at PCC (point of common coupling) [9].

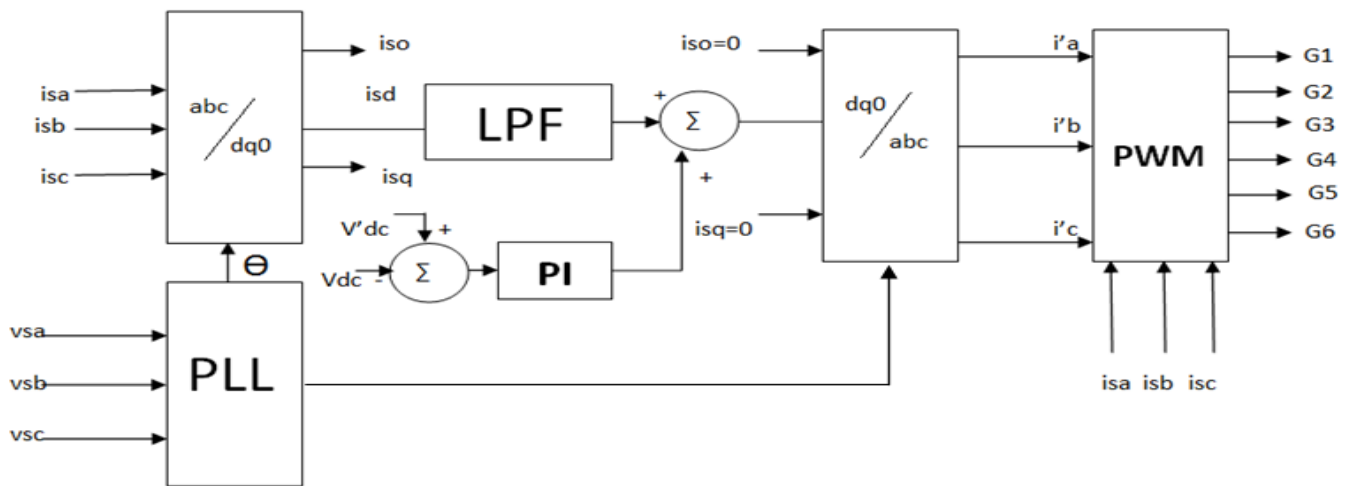


Fig.4 Control Scheme for Shunt APF

## V. SIMULATION RESULTS AND CONCLUSION

First of all consider the system with PV- inverter and nonlinear load, both are injecting harmonics in to distribution grid. The simulation consisting of nonlinear load and photovoltaic system without UPQC is shown below.

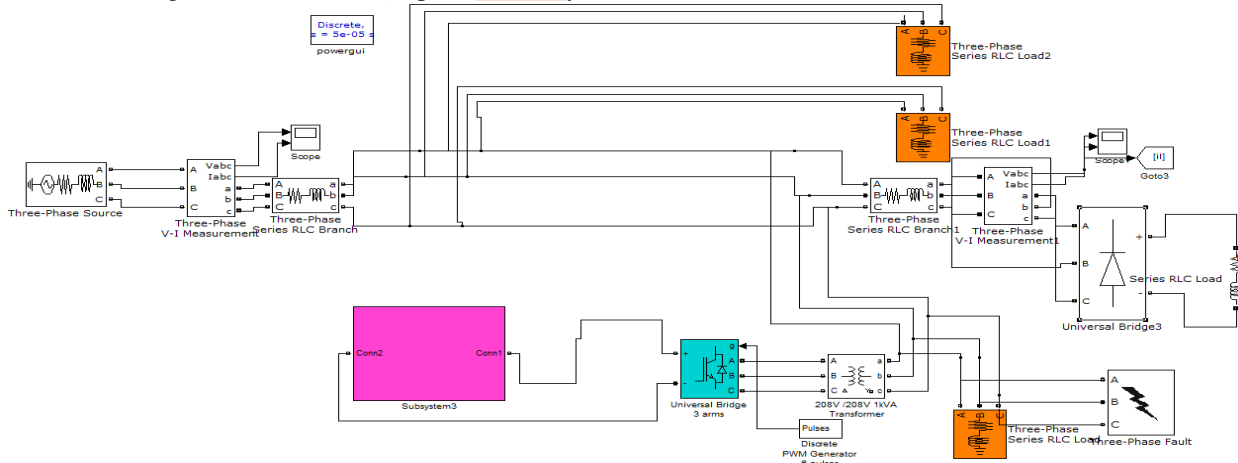


Fig.5. Circuit consisting of PV panel and non linear load without UPQC

Now in this simulation the system is without UPQC therefore inverter connected to PV and non linear load both are injecting harmonics in to grid supply which are shown below with the help of THD measurement and waveform of line current without UPQC.

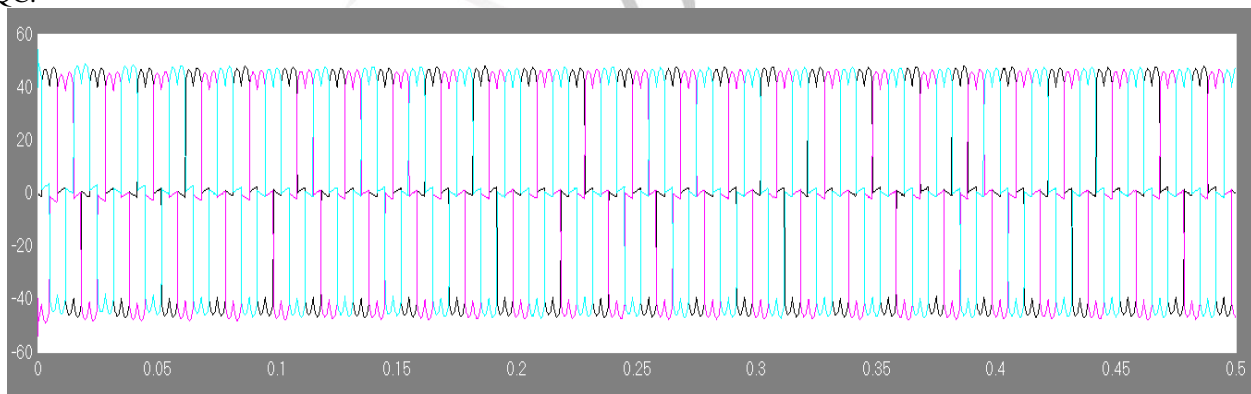


Fig.6 Line current waveform without UPQC

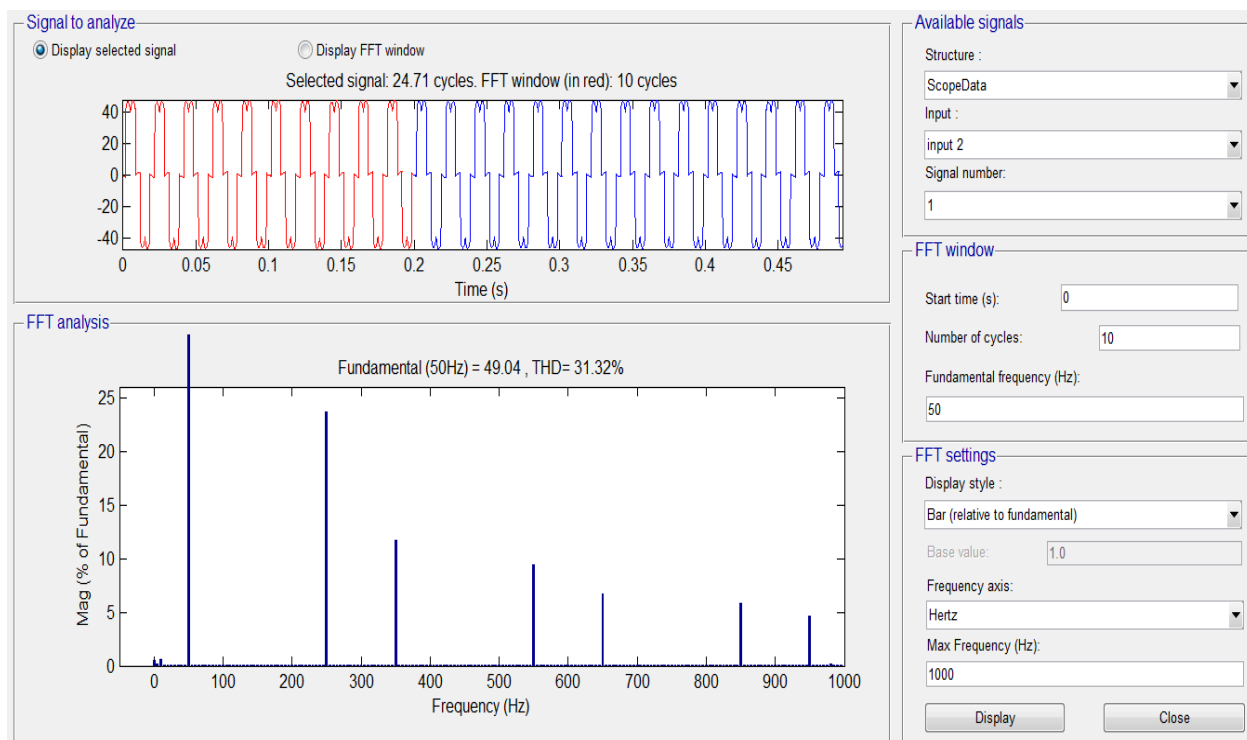


Fig.7 Line current THD without UPQC

Now UPQC is connected at point of common coupling which is shown below with the help of MATLAB simulation diagram.

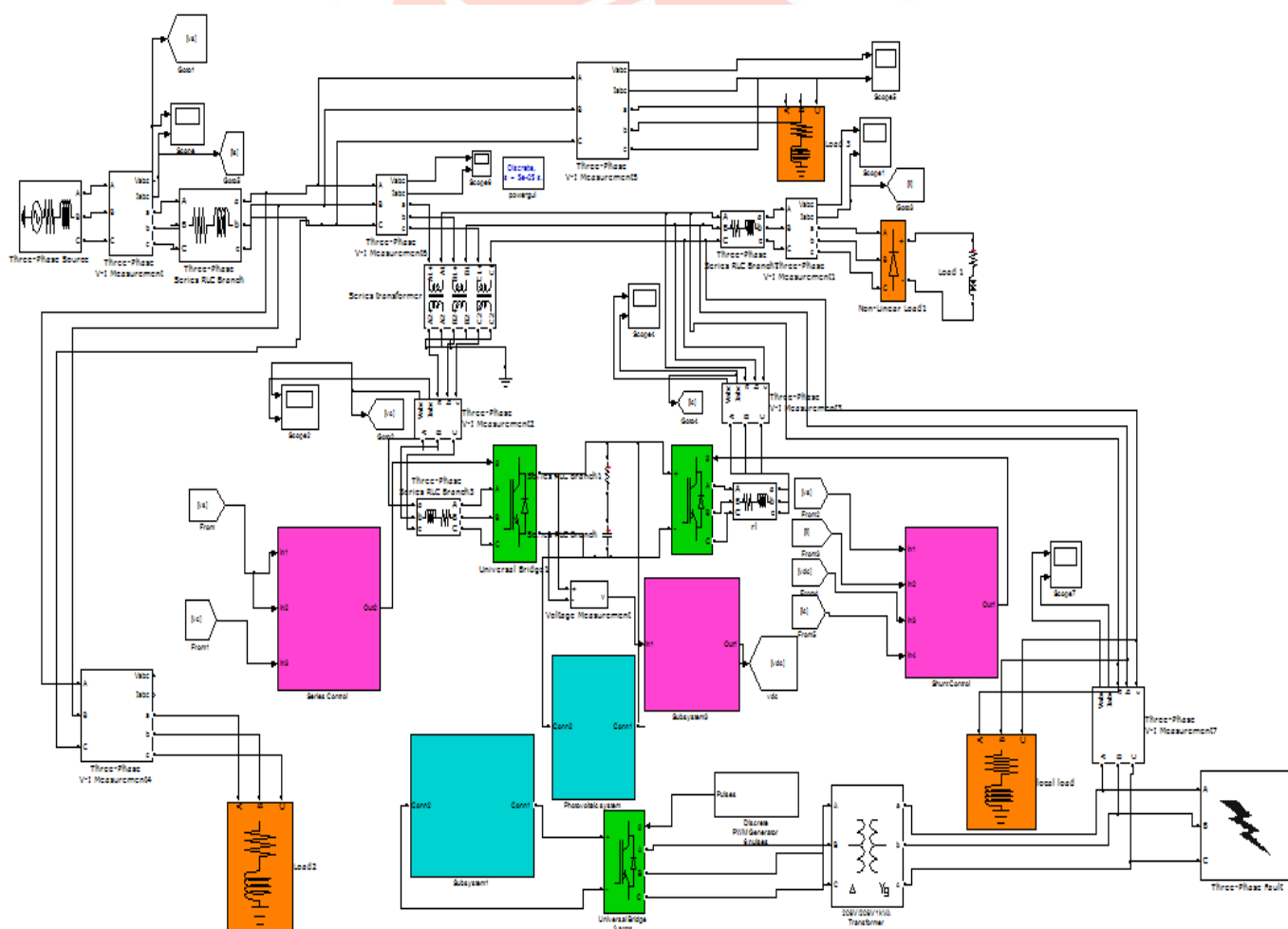


Fig.8 Simulation of circuit with UPQC

Now the fig shown above shows the compensated circuit with UPQC. In this the UPQC is connected to improve the voltage and current profiles of the system. Now after connecting UPQC the THD level of line current is shown below with the help of waveform and THD measurement.

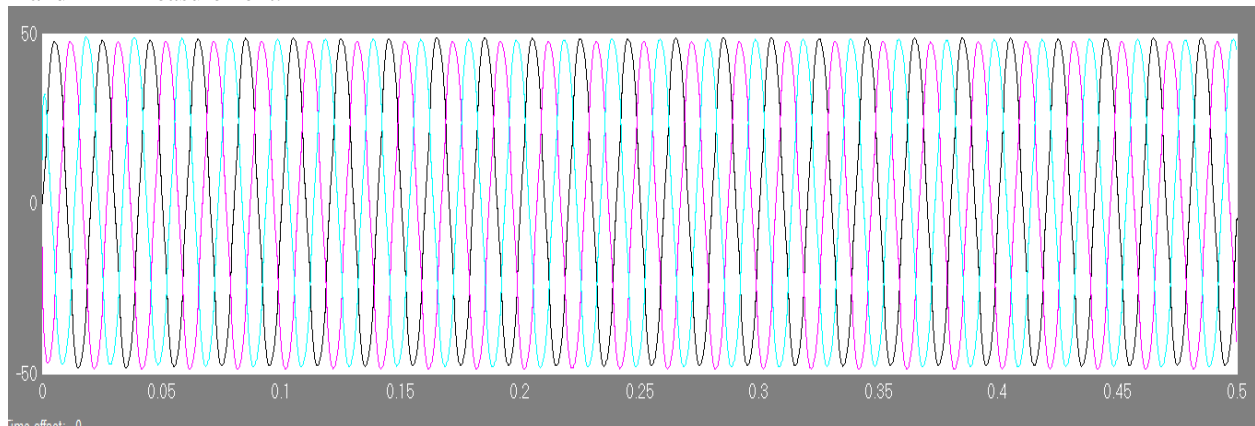


Fig.9 Line current waveform with UPQC

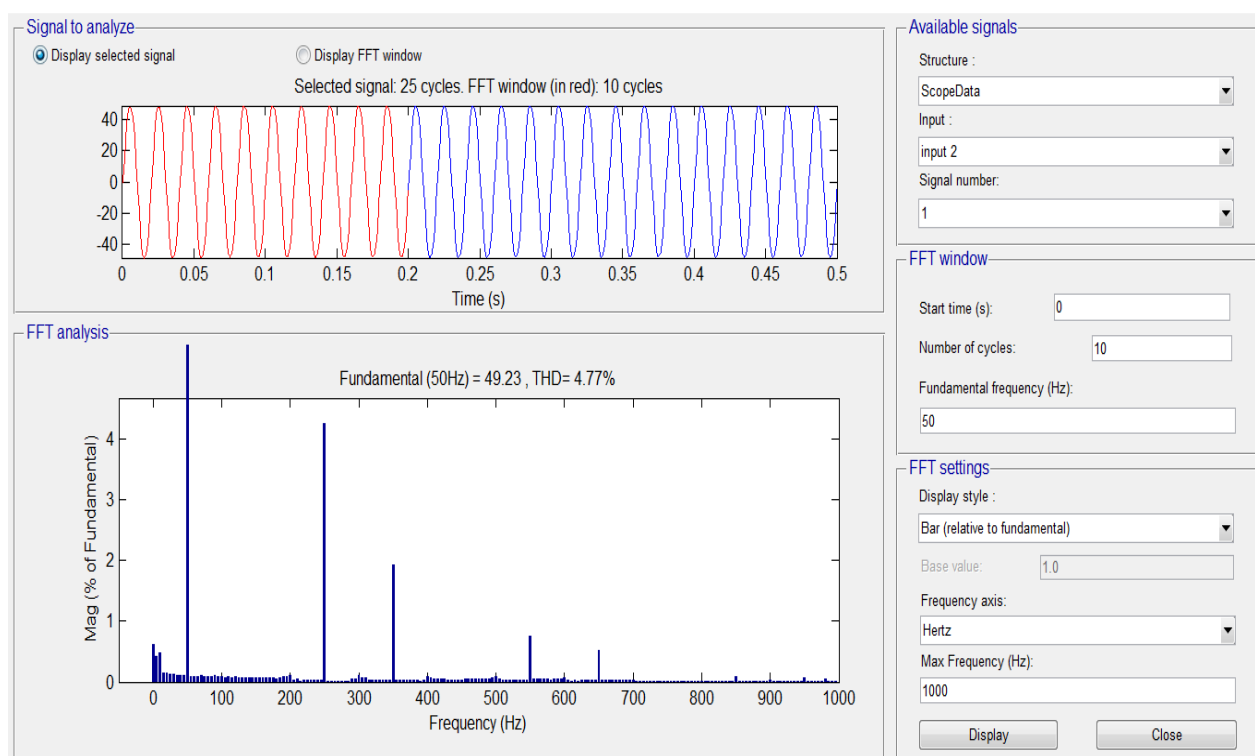


Fig.10 Line current THD with UPQC

It shows that the THD level reduces to 4.77% from 31.32% after connecting UPQC at the point of common coupling. Now if there is any fault occur at distributed generation consisting of PV panel then the voltage profile of line gets disturbed which is shown below with the help of LLLG fault.

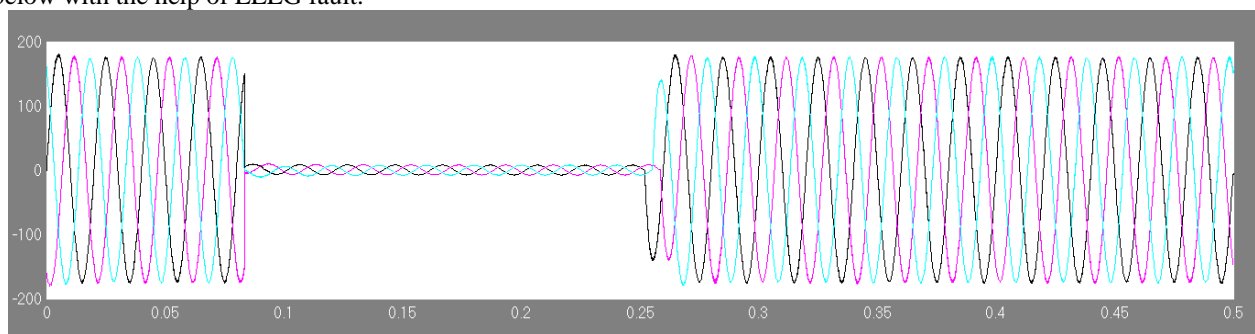


Fig.11 Line voltage waveform during LLLG fault

If we connect UPQC at PCC then series active power filter will inject voltage in series with the line and improve line voltage profile as shown below.

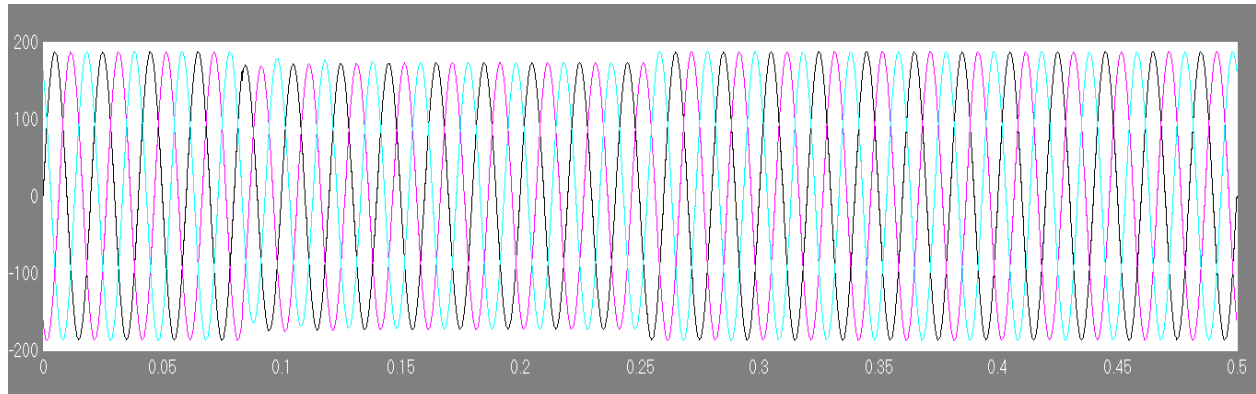


Fig.12 Line voltage waveform during LLLG fault with UPQC

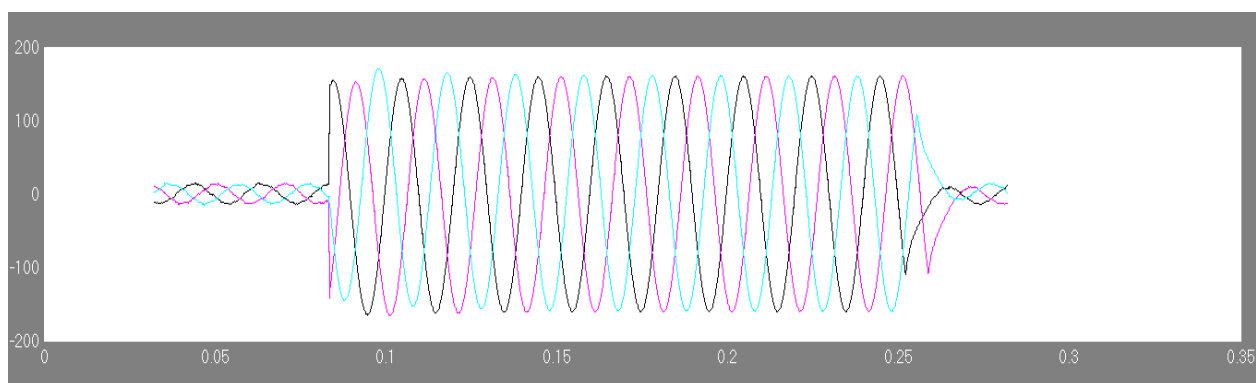


Fig.13 compensation voltage provided by UPQC during LLLG fault

So it shows that UPQC can improve both voltage and current profiles of system, by providing voltage compensation through series APF and current compensation through shunt APF. Therefore it can be effective in maintaining power quality of distribution networks consisting of non linear loads and distributed generation sources.

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