Comprehensive Validation and Debug Tool for verification of Radar Controller

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Abstract - Radar Controller (RC) developed for Surveillance Radar is an advanced GUI application which enables Radar operation and monitors health status of 14 functional subsystems and is equipped with intelligent automated error handling mechanism. RC being the only operator interface with the mission-critical system, meticulous validation of RC is imperative. A comprehensive software tool has been developed to simulate the entire Radar interface with RC and to enable thorough standalone verification of RC's wide-range of functionalities. The user-friendly icons-based tool is self-explanatory, and enables flexible and quick simulation of any type of error condition handled by RC, which is difficult to validate with the integrated system. The tool comprises of exhaustive GUI and time-stamped packet-capturing utility to simplify fault-finding and troubleshooting and makes the validation process self-reliant and independent of external tools. The software modules in the tool are generic and can be reused to validate similar software applications.

keywords - Radar Controller, Comprehensive Validation and Debug Tool

I. INTRODUCTION

Radar Controller (RC) developed for Surveillance Radar is a comprehensive and advanced GUI-based software application used for Radar operation and status monitoring [1]. RC is a self-reliant application and is equipped with intelligent error handling mechanism as shown in Fig. 1, to appropriately automate the error management process, exclusive of human intervention.

RC controls and monitors health status of fourteen functional subsystems of the Radar broadly classified as Antenna Control Unit, Power Distribution Unit, Transmitter, Receiver, Sensitivity Time Control, Waveform Generation Module, Signal Processor, Power Supply Modules, Power Monitoring Unit, Centroider, Tracker etc [1].

Systematic and complete validation of RC is essential as RC is the only operator interface with the mission-critical system. A comprehensive software tool has been developed to simulate the entire Radar interface, and to perform thorough standalone verification of RC and finally, to facilitate seamless integration of RC with the Radar system and ensure safe operation of the mission-critical system.



Fig 1. Overview of the Error Handling Mechanism in Radar Controller

II. COMPREHENSIVE VALIDATION AND DEBUG TOOL

Comprehensive Validation and Debug Tool (CVDT) is a sophisticated software tool which simulates the entire Radar interface with RC in a single software application. CVDT comprises of exhaustive GUI to enable meticulous standalone verification of RC's wide range of functionalities [1][2].

CVDT enables quick simulation of any type of error condition that occurs in the Radar system and systematically corroborates respective error management response from RC. CVDT facilitates flexible verification and validation process with simulation of various probable exceptional error conditions that are difficult to substantiate with the integrated Radar system [3].

Icons-based indications enhance user-friendliness and makes CVDT self-explanatory. CVDT provides real-time text-based error message display and simplifies fault-identification. CVDT includes in-built time-stamped UDP packet display utility to alleviate troubleshooting and make validation process self-reliant and independent of external tools.

CVDT software is developed in C# using .Net framework and is modularized, which makes the software modules reusable to validate similar Radar software applications.

III. IMPLEMENTATION

Critical Radar subsystem parameters are classified as prerequisites, interlocks, control parameters and transmission related parameters. RC is designed to initiate error handling scheme and intelligently take automatic appropriate action respectively for each error condition related to critical parameters as classified above. RC also periodically monitors the connectivity of subsystems and is intended to take appropriate action during failure condition.

Radar Interface Simulation

The Radar communication architecture is divided into multiple network domains based on the functionalities of Radar subsystems and the network load imposed. Real-time configuration of network interface is provisioned in CVDT.

CVDT GUI comprises of 12 pages for configuration of parameter status and values of 12 subsystems respectively. Each respective subsystem page is viewed by clicking on the respective arrow button. Subsystem parameters are configured based on the test cases and 'Update' button is clicked for updating the configurations in the status response to RC, as shown in Fig. 2.

Subsystems comprising of only overall subsystems health status are configured using dropdown menu as shown in Fig. 2. Control command parameters that are not a part of any status response are verified through 'OTHER CTRLS' page. Timer based simulation of Antenna position as well as Output power value during Receiver calibration is also available as shown in Fig. 2.

•		Compi	rehensive Validat	ion and Debu	ıg Tool			- 🗆 🗙
IP Config Packet Details	Ra	dar Su omprehen	ubsyste sive Validat	ems S ion and I	Simulato Debug Tool	r	ST ST	
ACU & PDU			S	olid State P	ower Amplifier			
SSPA	Amp Mod 1	ОК ✓	Amp Mod 2	ок 🗸	Amp Mod 3	№ТОК 🗸	Amp Mod 4	ок 🗸
	High Temp	ОК 🗸	High Temp	ОК ✓	High Temp	ок 🗸	High Temp	ОК 🗸
	Over Temp	ОК ✓	Over Temp	ок 🗸	Over Temp	№ТОК 🗸	Over Temp	ок 🗸
PMU	Over Curr	ОК 🗸	Over Curr	ок 🗸	Over Curr	ок 🗸	Over Curr	ок 🗸
🗹 RPSU 📃	PA1 High Temp	ОК ✓	PA5 High Temp	ок 🗸	PA9 High Temp	ок 🗸	PA13 High Temp	OK v
🗹 STC & SLC 🛛 🗖	PA1 Over Temp	ОК ✓	PA5 Over Temp	ок 🗸	PA9 Over Temp	ок 🗸	PA13 Over Temp	OK 🗸
🗹 WTG 🛛 🗖	PA1 Over Curr	ок 🗸	PA5 Over Curr	ок 🗸	PA9 Over Curr	ok 🗸	PA13 Over Curr	ок 🗸
I SP	PA2 High Temp	0К 🗸	PA6 High Temp	ОК ✓	PA10 High Temp	ок 🗸	PA14 High Temp	ОК 🗸
	PA2 Over Temp	ОК 🗸	PA6 Over Temp	ок 🗸	PA10 Over Temp	№ТОК 🗸	P. 14 Over Temp	ок 🗸
	PA2 Over Curr	ОК 🗸	PA6 Over Curr	OK ♥	PA10 Over Curr	ок 🗸	PA14 Over Curr	ОК 🗸
	PA3 High Temp	ОК 🗸	PA7 High Temp	ок 🗸	PA11 High Temp	ок 🗸	PA15 High Temp	ок 🗸
	PA3 Over Temp	ОК 🗸	PA7 Over Temp	ок 🗸	PA11 Over Temp	ок 🗸	PA15 Over Temp	ок 🗸
	PA3 Over Curr	ок 🗸	PA7 Over Curr	ок 🗸	PA11 Over Curr	ок 🗸	PA15 Over Curr	ок 🗸
✓ RVIC OK ✓	PA4 High Temp	ок 🗸	PA8 High Temp	ок 🗸	PA12 High Temp	ок 🗸	PA16 High Temp	ок 🗸
✓ DVG OK v	PA4 Over Temp	ОК ✓	PA8 Over Temp	ок 🗸	PA12 Over Temp	ок 🗸	PA16 Over Temp	OK 🗸
	PA4 Over Curr	ок 🗸	PA8 Over Curr	ок 🗸	PA12 Over Curr	ок 🗸	PA16 Over Curr	ок 🗸
	RF Driver	🔺 ОК 🗸	RF Drv RF Input	🔺 ОК 🗸	Combiner 🔺	ок 🗸	Amp Mod	▲ NOTOK ✓
RX CAL -65 dBm	Drv1 High Temp	ок 🗸	Drv2 High Temp	ок 🗸	RF Output	ок 🗸	Tx Over Duty	🔺 ок 🗸
ANTENNA 0.0 deg	Drv1 Over Temp	ОК 🗸	Drv2 Over Temp	ок 🗸	VSWR 🔺	ок 🗸		
	Drv1 Over Curr	ок 🗸	Drv2 Over Curr	ок 🗸	FlowSensor	ок 🗸		Update
	Page edited. Click on '	Update' button to sa	ave changes					
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Fig 2. Sample snapshot of CVDT to demonstrate subsystem parameter configuration

Simulation of error conditions

Critical Radar subsystem parameters are classified as prerequisites, interlocks, control parameters and transmission related parameters. RC is designed to initiate error handling scheme and intelligently take automatic appropriate action respectively for each error condition related to critical parameters as classified above. RC also periodically monitors the connectivity of subsystems and is intended to take appropriate action during failure condition.

a. Subsystem connectivity failure: Status response from subsystem is disabled when respective subsystem's checkbox is unchecked to simulate subsystem connectivity failure condition, as shown in Fig. 3.

ACU & PDU	\Rightarrow
SSPA	\Rightarrow
SSPA PS	\Rightarrow
PMU	\Rightarrow
	\Rightarrow
STC & SLC	\Rightarrow

Fig 3. Subsystem connectivity failure simulation

b. Prerequisite failure: Prerequisite parameters as indicated with orange triangle icon are configured accordingly to simulate error condition and the automated action performed by RC to transition Radar to NOT READY state is verified, as shown in Fig. 4.

Δ	NOTOK	¥	
Pr	erequisite	7	۷
-	1	-	A
Δ	ОК	۷	
Δ	ОК	×	
	60		۷
	3.1		A
Δ	ОК	¥	
		 ▲ NOT OK Prerequisite 1 ▲ OK 60 3.1 ▲ OK 	 ▶ NOT OK ▼ Prerequisite 1 △ OK ▼ 60 3.1 △ OK ▼

Fig 4. Prerequisite failure simulation

c. Interlock failure: Interlocks as indicated with red triangle icon are configured to simulate error condition when transmission is enabled by RC and the automated action executed by RC to systematically discontinue transmission is verified, as shown in Fig. 5.

PMU		ок	¥
Output Power	-	ОК	~
Reflected Power	-	ОК	~
VSWR	-	NOTOK	~
Page edited. Click or	ln	terlock P	n to s

Fig 5. Interlock failure simulation

d. Control parameter mismatch: Control command related error conditions are provisioned to be simulated in two ways and automated action accomplished by RC is verified.

One way to simulate parameter mismatch error condition is on command initiation i.e. checkbox of the respective control parameter is unchecked to simulate rejection of control command, as shown in Fig 6.

Sector Tx	OFF 🗸	
✓ Width	±80 🗸	deg
Centre	0	deg
PC1		
Pulse width R M	4	US

Fig 6. Control parameter mismatch simulation on command initiation

Other way of simulating the error condition is during Radar operation i.e. the value of the control parameter in textbox or dropdown menu is modified to simulate error during Radar operation, as shown in Fig 7.



Fig 7. Control parameter mismatch simulation during Radar operation

e. Transmission related parameter error: Parameters significant to transmission as grouped with blue background are tightly coupled. These parameters are crucial and are intended to follow a specified sequence which is verified by varying their status and simulating required test conditions, as shown in Fig. 8.

Controls		
Tx	Disable 🗸	
Radiate	OFF 🗸	
Sector Tx	OFF 🗸	Ĩ

Fig 8. Transmission related parameter mismatch simulation

f. Shared control parameters error: Control commands shared between several subsystems as indicated with 'M' icon are modified based on the test case to simulate error and validate action performed by RC as shown in Fig. 9.

Pulse width	RM	US	
CPI1 PRT	RM	us	
CPI2 PRT	RM	us	
CPI3 PRT	RM	us	
CPI4 PRT	RContro	l used by more than	one subsystem
	R		one subsystem

Fig 9. Shared control parameter error simulation

g. Range related error: The Radar is configured for Long range (long pulse) and Medium range (short pulse) operation. Certain parameters have varied values based on the range selection as indicated with 'R' icon are modified as required by the test scenario to simulate error as shown in Fig. 10.

CFAR Threshold	0	✓ dB	CPI
ON Range [R 100	km	CPI
🗌 Video	Varies ba	ased on 'Range	e' selection
Plot Related			
FFT Window ON Rng [R 100	km	
FFT Filter Removal	0	¥	
Canceller	2	~	

Fig 10. Shared control parameter error simulation

Real-time error message display

Control command sent by RC is validated in real-time by CVDT with the set of acceptable values and error message is displayed immediately to facilitate quick identification of error, as shown in Fig. 11.



Packet display utility

In-built packet display utility provides time stamped details of the UDP datagram sent by RC as a part of Control command or Status query to ease troubleshooting, as shown in Fig. 12.

Subsystems Chiller		Centroider - LR	Centroider - HR	Centroider - HR		Tracker	
0:BE 1:0 2:5A 3:FF 4:A0 5:1 6:0 7:0 9:0 10:0 11:0 11:0 12:0 13:0 14:0 15:0 16:0 17:0	~	0:32 1:0 2:5A 3:FF 4:C2 5:1 6:0 7:0 8:0 9:0 10:70 11:17 12:0 13:0 14:64 15:0 16:3 17:0	0:32 1:0 2:5A 3:FF 4:C2 5:1 6:0 7:0 8:0 9:0 10:70 11:17 12:0 13:0 14:64 15:0 4:C2 13:0 14:64 15:0 17:0 14:64 15:0 17:0 1	<	$\begin{array}{c} 0:32\\ 1:0\\ 2:5A\\ 3:6\\ 4:DD\\ 5:1\\ 6:0\\ 7:0\\ 8:0\\ 9:0\\ 11:4\\ 12:0\\ 13:0\\ 11:4\\ 12:0\\ 13:0\\ 14:E8\\ 15:3\\ 16:0\\ 17:0\\ \end{array}$	~	
3:55:20	00:00:00	3:55:40	3:55:46		3:56:1		
Status Reque	est						
Subsystems	: 3:56:55						
Chiller	: 0:0:0						
Centroider	: 3:56:54						
Radar Display	: 3:56:12						
Rx Calibration	: 0:0:0						

Fig 12. Packet Display Utility

IV. SALIENT FEATURES

Some of the salient features of CVDT are listed below:

- a. CVDT provides flexibility of enabling and disabling subsystem interface through GUI and facilitates systematic validation of each subsystem interface as well as entire Radar interface with RC
- b. Icons-based CVDT enables quick and self-reliant identification of important Radar subsystem parameters without document reference and effectively reduces the software testing time [10]
- c. Wide range of hints in the form of tooltips and text indications makes CVDT self-explanatory.
- d. CVDT ensures meticulous validation of RC by simulation of any combination of errors which is difficult to simulate in the integrated system
- e. Packet display utility in CVDT provides useful debug interface and eases troubleshooting without depending on any external packet capturing tool.
- f. CVDT is developed in modular form which is easily maintainable and reusable.

V. CONCLUSION

CVDT is a self-reliant, self explanatory and user-friendly tool which plays a vital role in comprehensive and rapid standalone verification and validation of Radar Controller software used for operation and health monitoring of mission-critical system. CVDT ensures the robustness and appropriateness of RC with flexibility to simulate exhaustive test scenarios and facilitates seamless integration of RC with the Radar system, and ultimately ensures safe operation of the Radar system. CVDT is developed in modular form with easily configurable software modules. Considering that parameters across solid state Radars are generic in nature, with modifications or additions, this tool can be easily extended for validating Radar Controller applications of other similar Radars.

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185