

# ARM7 based Smart bus Passenger-Alert System using GSM with GPS based Location Identification

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**Abstract** - In this paper we present a solution to the main problems faced by passengers who use bus as a means of their transportation. The main reasons for the discomfort are unpredictable bus schedules have made it virtually impossible to estimate the time of arrival at the required destination. This paper present an intelligent real time alarming system which senses the destination location which is taken as input from the passenger. This system also includes android mobile alert application. After the passenger reached the destination, bus system alert the passenger through SMS with android application in passenger mobile and also assisted with alarming system in case of excessive amount of temperature in case of smoke identification which leads to the fire accidents .this asbeen an serios problem in ac buses now adays.

**Keywords** - Field Programmable Gate Array (FPGA), GPS Receiver, Location Identification, Embedded System.

## I. INTRODUCTION

Unpredictable bus schedules have made it virtually impossible to estimate the time of arrival at the required destination. The main problem about the transportation is the un-certainty of waiting time due to traffic jams. This may lead to a lot of discomfort in identifying when the destination has been reached, especially during the night hours Unfamiliarity with a region also makes it uneasy for passengers to alight the train at the required destination. Country .Alerting system uses GSM and GPRS for sending information, GSM is mostly used as compared to the GPRS. GSM is used to inform the user about exact location of vehicle.

GPS tracking was initially developed for military purposes, but it is now widely used for variety of applications like cars, golf carts and even cell phones, because of its versatility with even smaller dimensions and weights coupled with the availability of solar cell power, these GPS tracking systems can be used in number of applications and situations. Location identification using GPS has seen a sharp rise owing to its simplicity and increasing affordability of GPS based devices

Embedded Systems are basically computer systems used for a specific purpose. It has a lot of advantages, which are designed to do meet real time performance constraints. This design strategy also allows the system hardware to be simplified and the streamlined make-up of embedded systems allow their components to be less expensive and so costs are reduced. The hardware is usually in the form of small computerized parts in larger devices which serve a general purpose and the program instructions for embedded systems run with limited computer hardware resources, little memory and small or even non-existent keyboard or screen. These systems do not involve the redundant programming as in other system models.

Field Programmable Gate Arrays (FPGAs) offer significant advantages in high performance and low volume applications. They also offer high flexibility in hardware programming and can be easily reconfigured as per the requirements at a particular point of time without much change in the external circuitry FPGA don't feature built-in peripherals, instead, peripherals can be logically programmed. FPGAs are very powerful in this regard, as they contain raw logic gates, and any hardware can be configured into the gates.

Keeping all the above factors quoted in mind, a novel embedded system has been designed to resolve this problem using Field Programmable Gate Arrays (FPGA) and a GPS receiver to alert passengers in bus when the intended destination is reached so that passenger can comfortably alight at the destination.

## II. OBJECTIVE AND TOOLS USED

**Project Objective:** The main objective of this project is to design an system that trigger an alarm in passenger mobile when that particular passenger destination is been reached with help of an android facilitated with GSM and GPS for location identification

### **Tools Used:**

Simulation Software: KEIL IDE is used for design and implementation for the simulation.

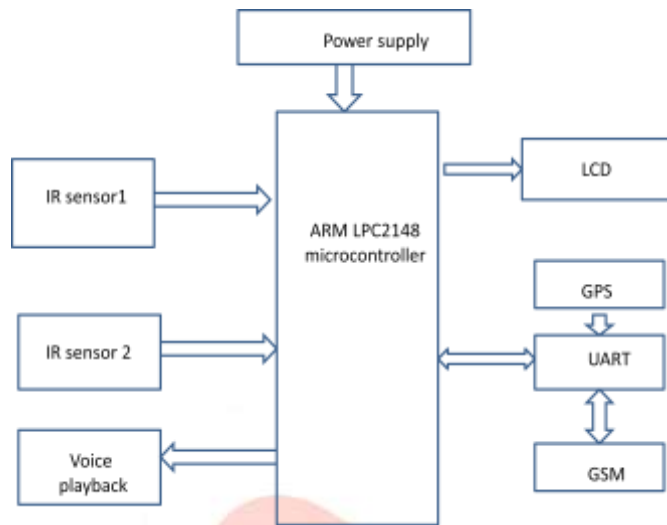
Hardware requirement: ARMLPC2148, IRsensor, Gassensor, GPS and GSM.

## III. APPROACH AND METHODOLOGY

In this design the input is given by the passenger to device connected to the bus which in turn is linked with the GPS system for the purpose of location identification. Once the destination of the passenger is reached the alarm is triggered in passenger's mobile through an SMS which is done with the help of GSM system.

#### IV. PROPOSED SYSTEM

A novel embedded system has been designed interfacing a GPS receiver with a host Field ARM LPC2148 board



The embedded system designed and implemented on the FPGA hosted in ARMLPC2148 employs a TSK3000A processor. Fig. 3 is a schematic that describes the major hardware blocks used in the FPGA. The TSK3000A processor is a 32-bit, soft-core, RISC processor and is compatible with the Wishbone bus system. The processor is employed with the other hardware viz. UART, SPI master controller, touch screen pointer context, Thin Film Transistor (TFT) display and SRAM. The UART block is a serial communications port (Wishbone-compliant), providing serial communication with hardware handshake and FIFO buffers. The SPI master controller enables communication with multiple slave display, supporting peripherals. The pointer component detects the status of stylus (for touch screen input) and feeds it to the processor. The TFT display controller provides a resolution of 240x320 and 320x240. This controller also has a timing unit which is capable of generating all required control and timing signals. The SRAM controller provides a simple interface between asynchronous static RAM and a 32-bit processor. The required destination is selected by the user by means of an on-board touch screen interface. Once the destination is sensed, an alarm is triggered in passenger mobile through an SMS.

#### V. HARDWARE IMPLEMENTATION

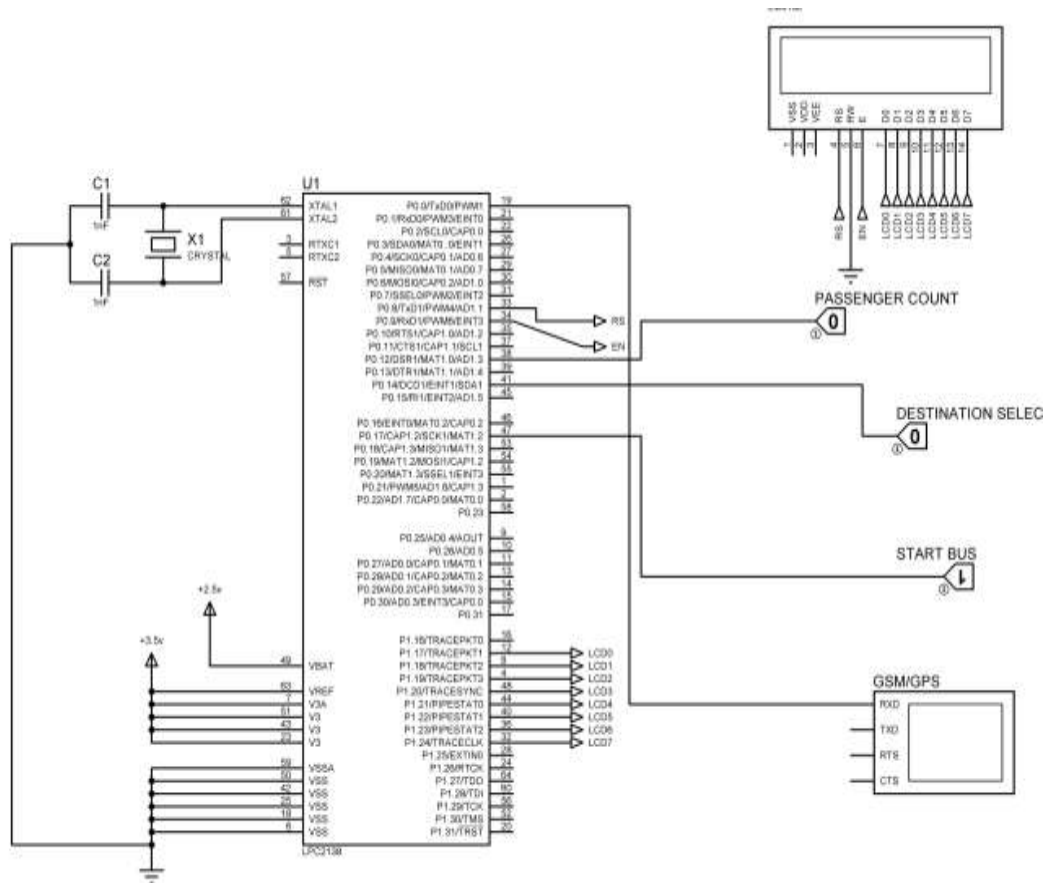


Figure: Circuit diagram

In this circuit diagram, LCD data pins connected from P0.17 to P0.24. RS and RW pins connected as follows P0.8 and P0.9. The GSM module is communicate the microcontroller with mobile phones through UART. To communicate over UART or USART, we just need three basic signals which are namely, RXD (receive), TXD (transmit), GND (common ground). In LPC2148 Primer Board contains two serial interfaces that are UART0 & UART1. Here we are using UART0 (P0.0). The GSM modem is being interfaced with the microcontroller LPC2148 SPI 32-bit interface between the processor and to the P0.12. Destination selection switch connected to P0.14. vehicle start button connected to P0.17.

VI .RESULT

